

Fig. 17E

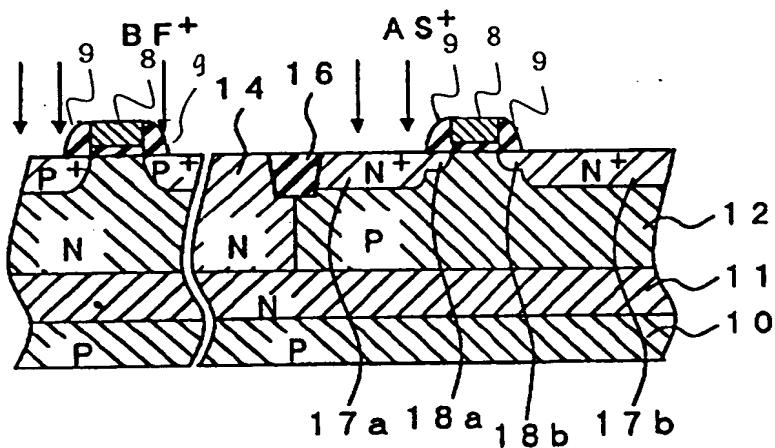


Fig. 17F

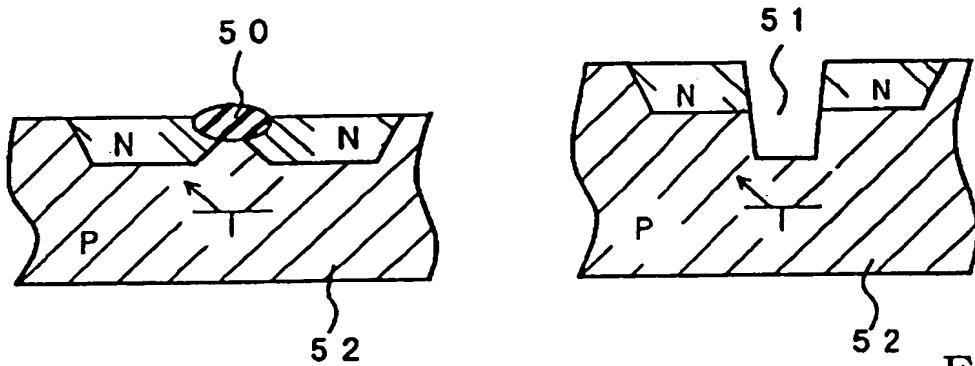


Fig. 1A
PRIOR ART

Fig. 1B
PRIOR ART

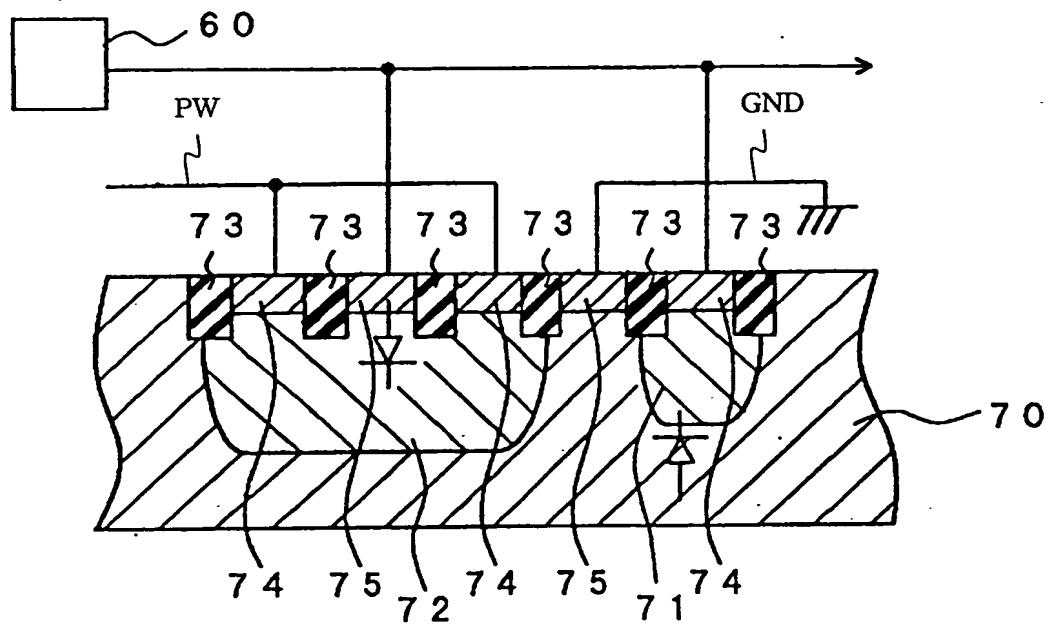


Fig. 2
PRIOR ART

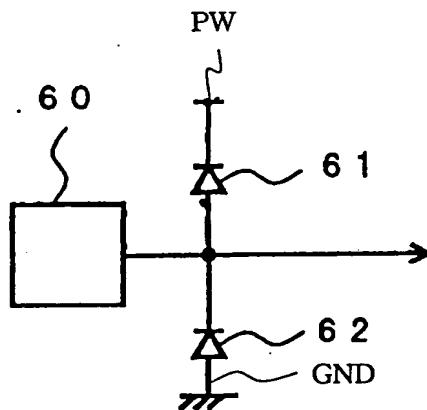


Fig. 3
PRIOR ART

Title: Semiconductor Device Having Protection Circuit Implemented by Bipolar
Transistor for Discharging Static Charge Current and Process of Fabrication
Inventor: Kaoru NARITA
U.S. Appln. No. 09/421,273

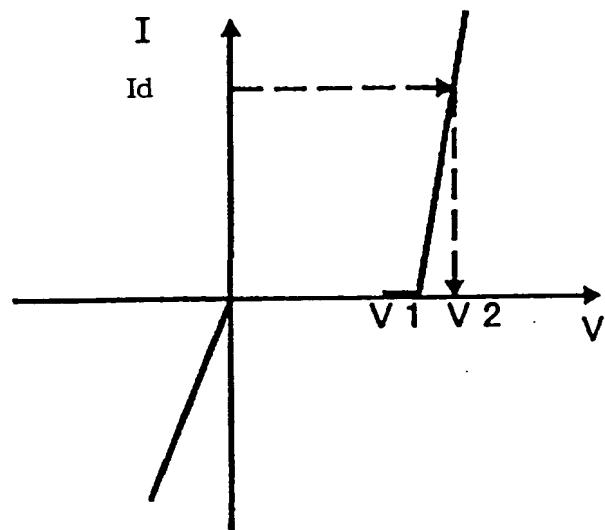


Fig. 4
PRIOR ART

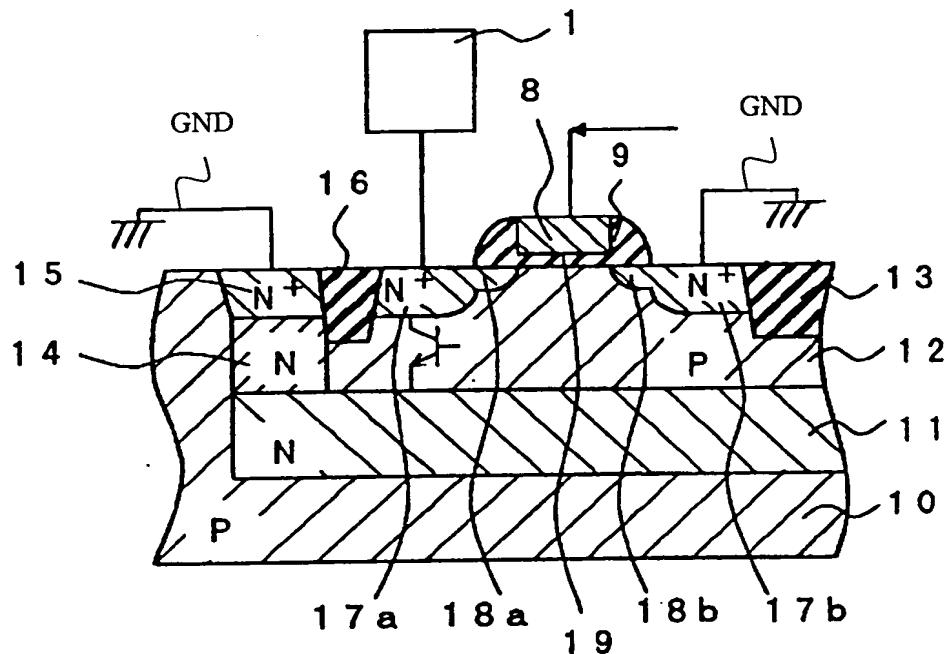


Fig. 5

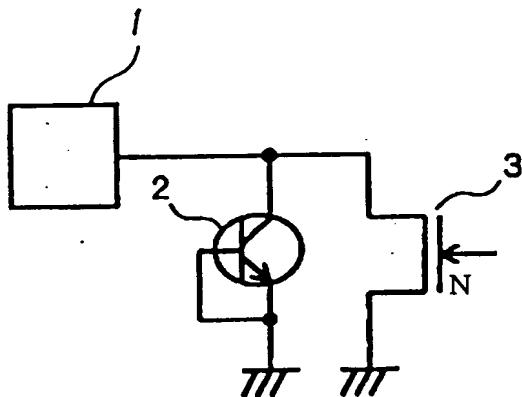


Fig. 6

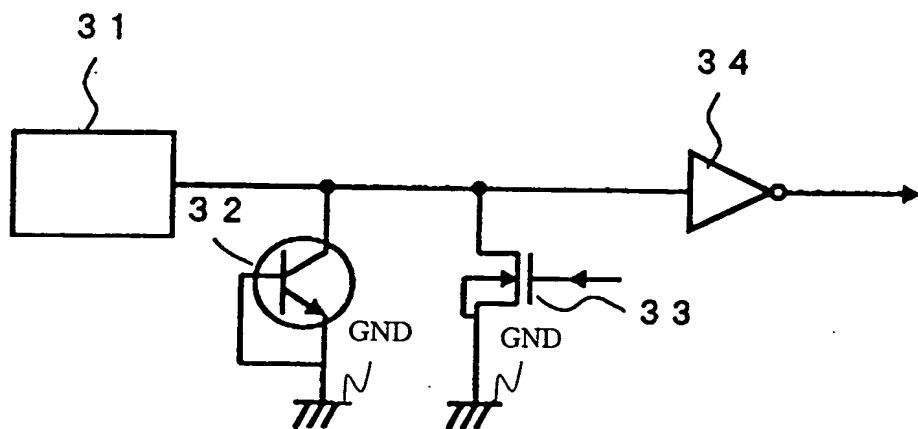


Fig. 15

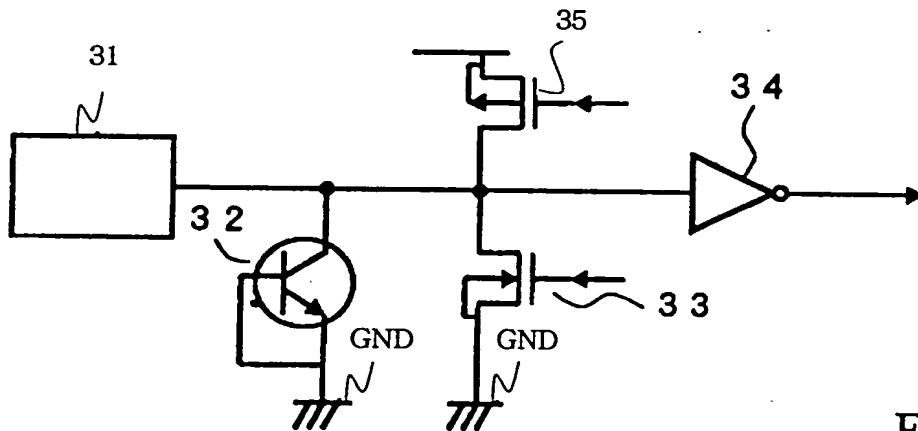


Fig. 16

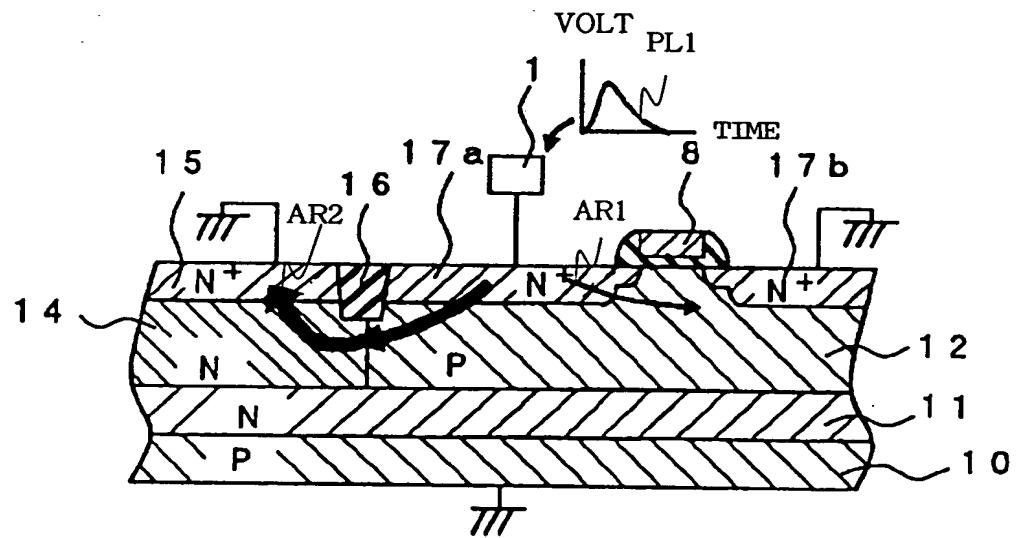


Fig. 7

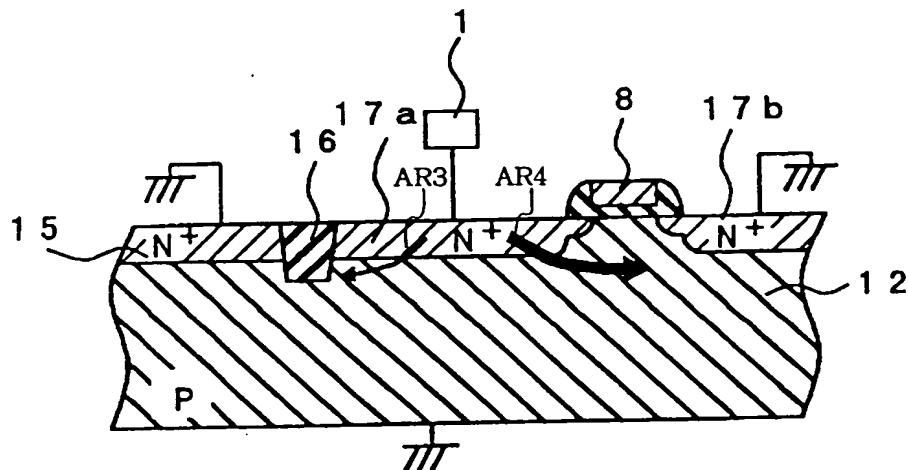


Fig. 8
PRIOR ART

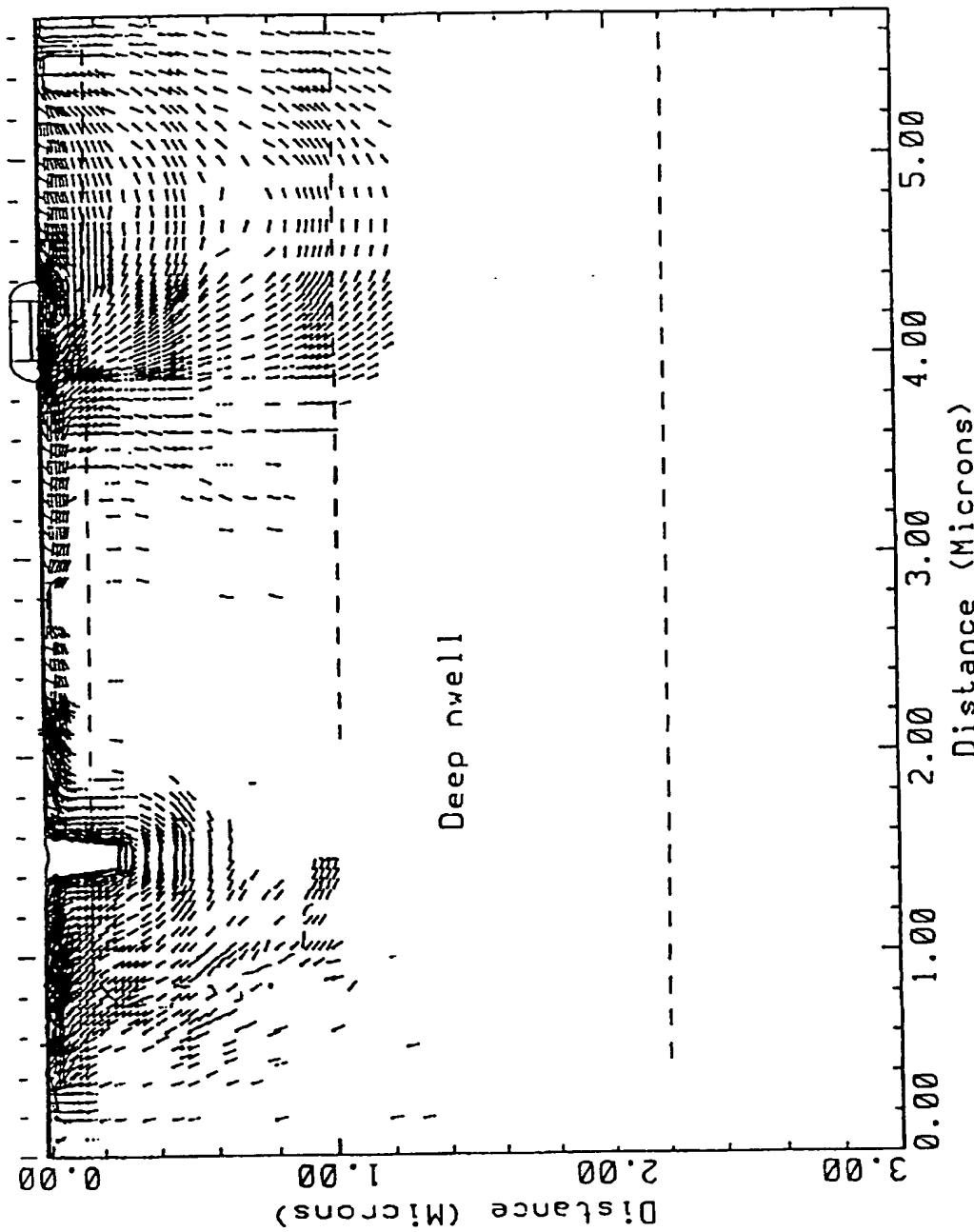


Fig. 9

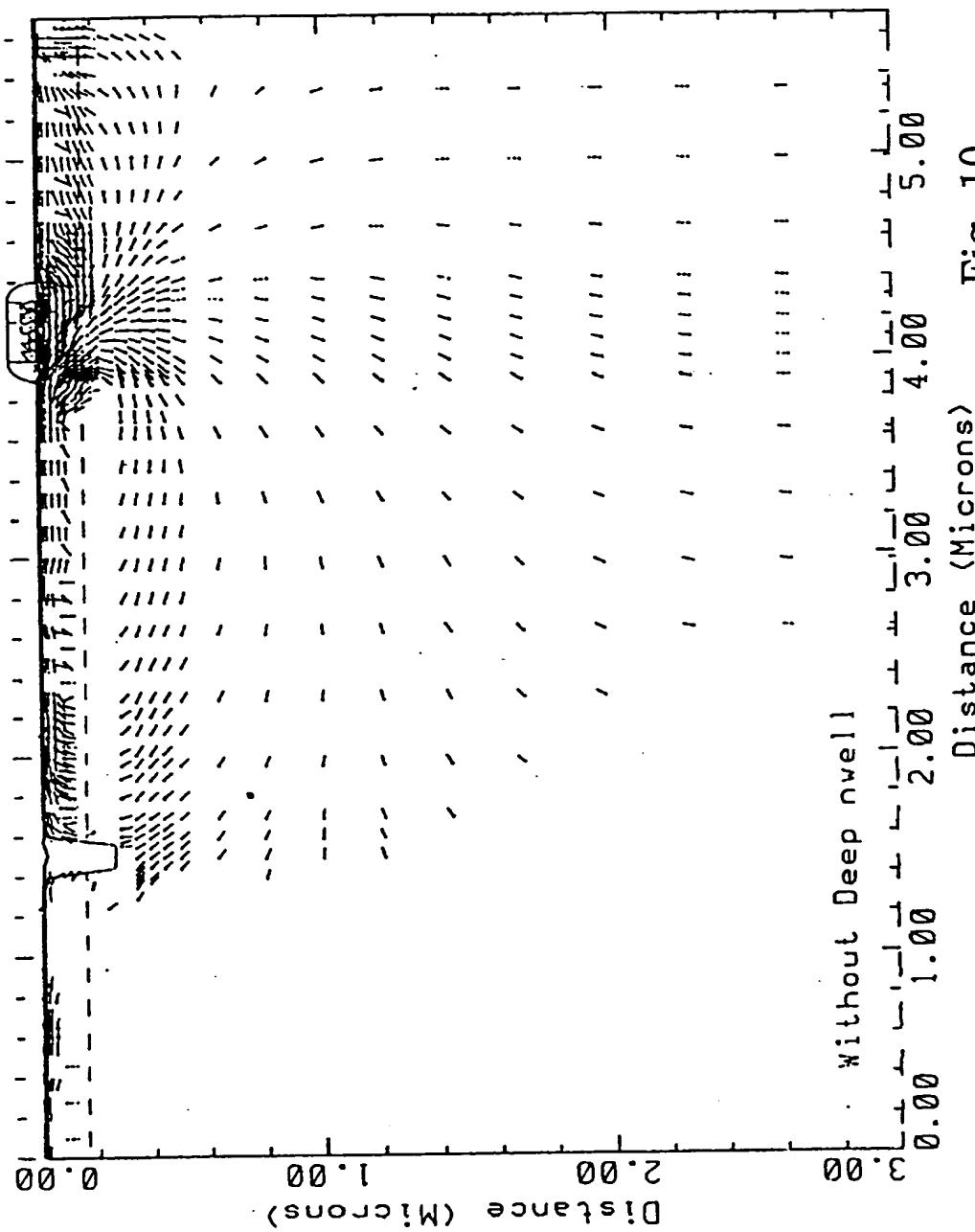


Fig. 10
PRIOR ART

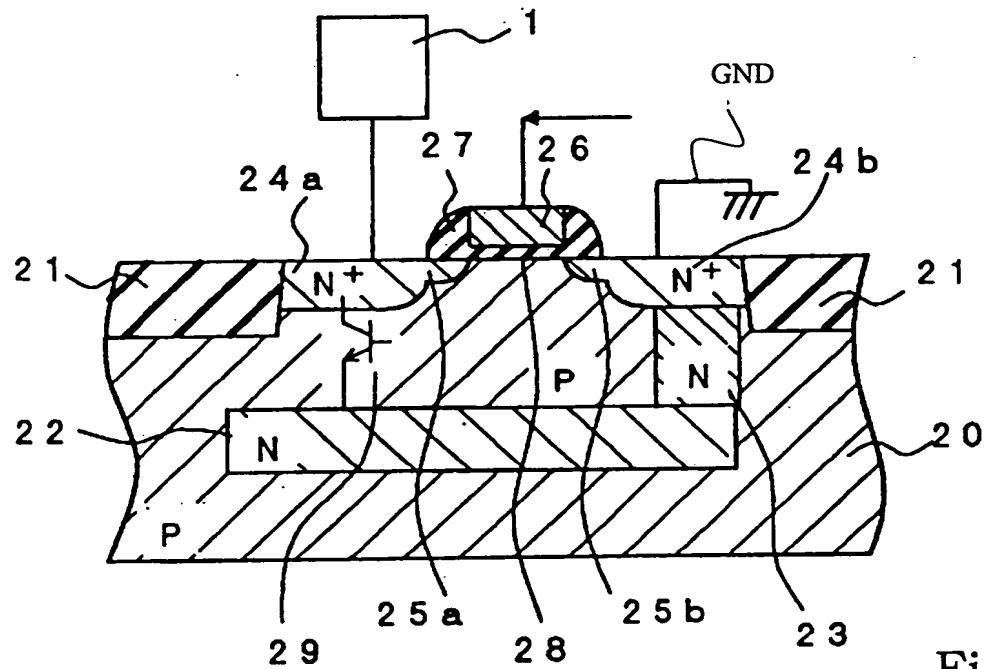


Fig. 11

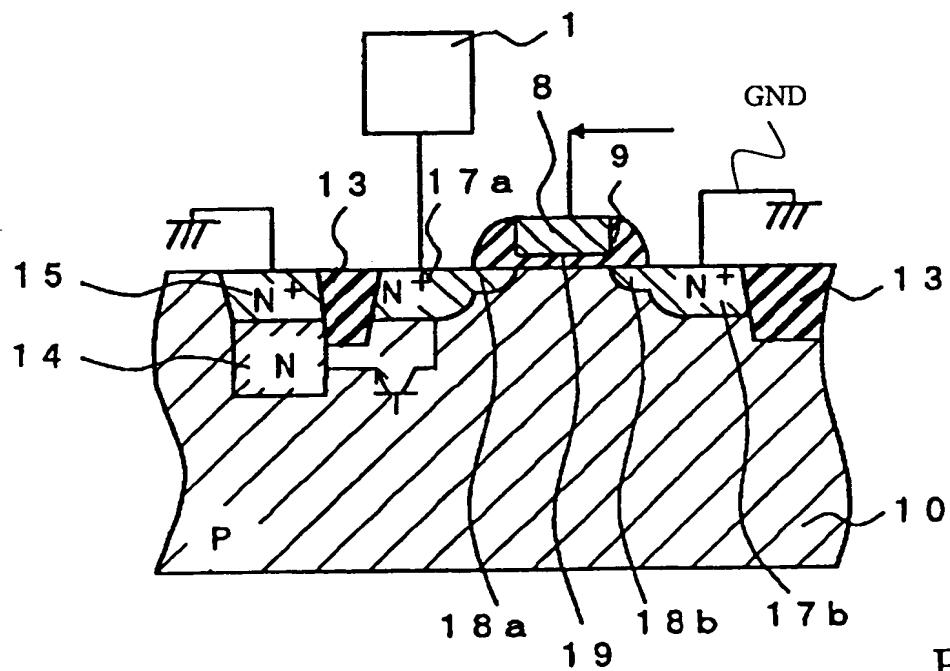


Fig. 12

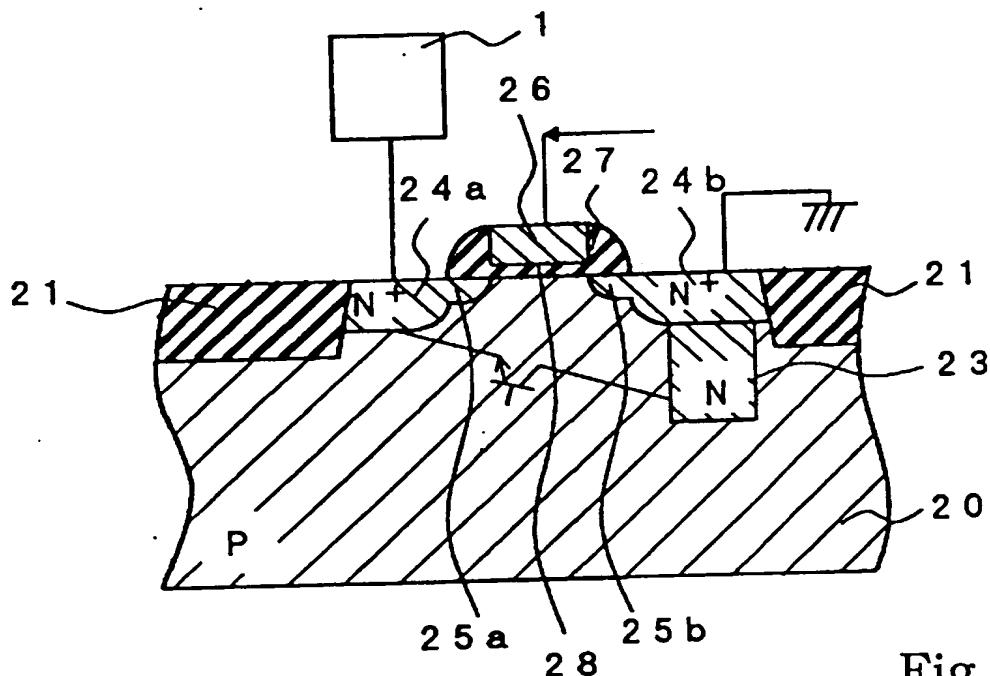


Fig. 13

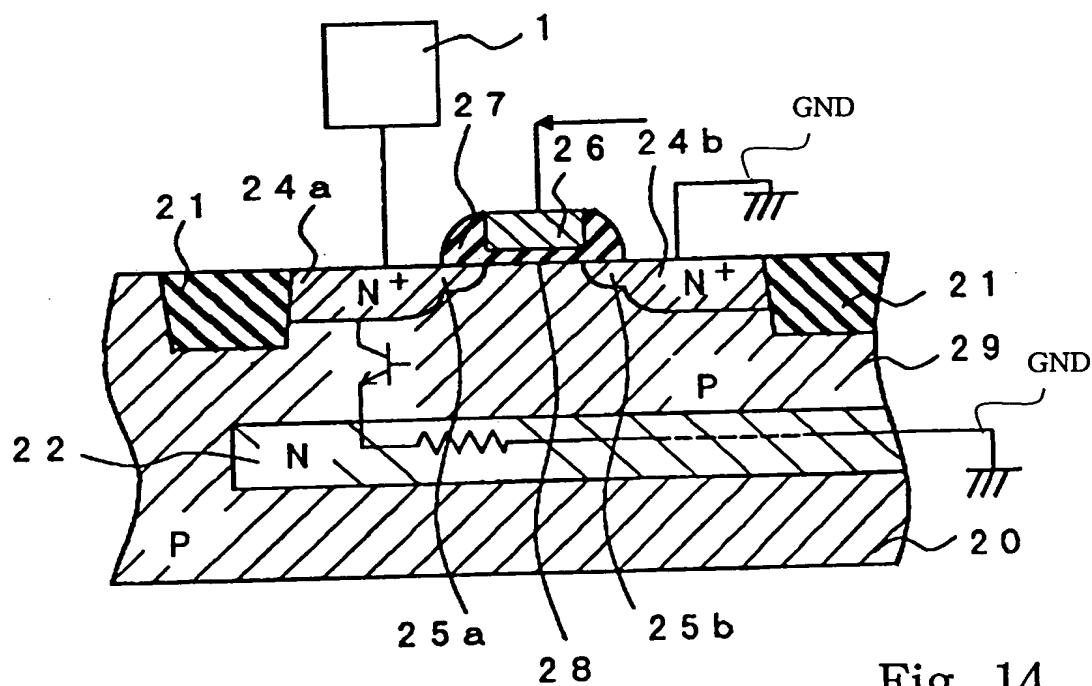


Fig. 14

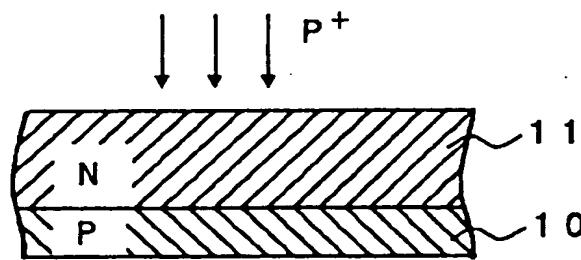


Fig. 17A

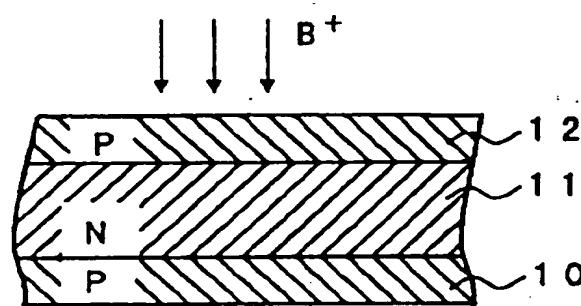


Fig. 17B

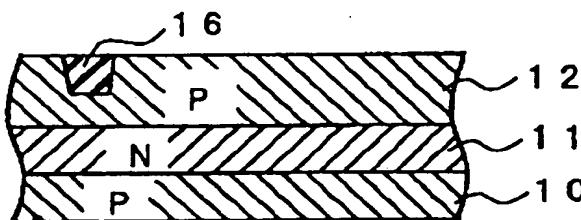


Fig. 17C

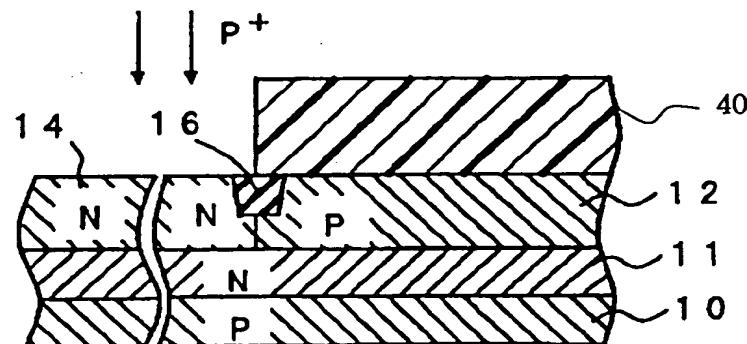


Fig. 17D